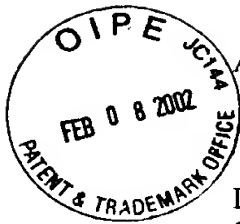


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Attorney Docket No. 5649-874

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hag-ju Cho
Serial No.: 09/893,035
Filed: June 27, 2001

Group Art Unit: 2813
Examiner: Erik J. Kielin

For: METHODS OF MANUFACTURING INTEGRATED CIRCUIT DEVICES
THAT INCLUDE A METAL OXIDE LAYER DISPOSED ON ANOTHER
LAYER TO PROTECT THE OTHER LAYER FROM DIFFUSION OF
IMPURITIES AND INTEGRATED CIRCUIT DEVICES
MANUFACTURED USING SAME

Date: January 23, 2002

BOX NON-FEE AMENDMENT
Commissioner for Patents
Washington, DC 20231

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

This Amendment is responsive to the Restriction Requirement of December 27, 2001.

In the Claims:

✓
Please cancel Claims 14 - 24 without prejudice or disclaimer.

REMARKS

In response to the Restriction Requirement of December 27, 2001, Applicant hereby elects Invention I, corresponding to Claims 1 - 13, which are drawn to methods of manufacturing an integrated circuit device.

Applicant agrees that unpatentability of Inventions II and/or III, which correspond to Claims 14 - 17 and 18 - 24, respectively, would not necessarily imply unpatentability of Invention I. Accordingly, Applicant is making this election without traverse, and has cancelled Claims 14 - 24 without prejudice or disclaimer.

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